Amendment and Response
Applicant: Jose L. Cervantes
Serial No.: 10/025,165
Filed: December 19, 2001
Docket No.: 10002896-1
Title: PORTABLE COMPUTER HAVING DUAL CLOCK MODE

REMARKS

The following remarks are made in response to the Office Action mailed October 4, 2004. Claims 1-26 were rejected. With this Response, claims 1, 11, 17, and 21 have been amended. Claims 25-26 have been cancelled. Claim 27 has been added. Claims 1-24 and 27 remain pending in the application and are presented for reconsideration and allowance.

Claim Rejections under 35 U.S.C. § 102

Claims 1-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Atkinson U.S. Patent No. 5,991,883.

Applicant submits that Atkinson fails to disclose the invention of Amended claim 1. Amended claim 1 recites a portable computer having a first power mode and a second power mode. The portable computer comprises a first memory bus, a second memory bus, and a control system coupled to the first memory bus and the second memory bus. The control system is configured to operate the first memory bus and the second memory bus at a first speed in the first power mode, and a second speed different than the first speed in the second power mode.

Atkinson discloses a system and process for power conservation in a portable computer system. When the application or hardware in use allows for reduced video performance, the refresh rate of the video graphics controller 2 is reduced to a level that allows practical use of the LCD display 3 or CRT display 4 but consumes much less power than a normal mode. (See Abstract; Fig. 1). The video graphics controller 2 interfaces directly to a memory 5. The memory is a VRAM for holding fonts, graphics, or text that is output to the display. The speed of the memory interface is controlled by an internal oscillator MCLK, whose frequency is programmable through an I/O location available to the CPU 1. (Column 7, lines 20-25; Fig. 1). MCLK is set by a single register named SR1F. A higher value in the register indicates a faster memory interface and subsequently higher video performance. A lower value in the register indicates a slower memory interface and poorer video performance. (Column 9, lines 48-56).

Applicant submits that Atkinson does not disclose the portable computer of amended claim 1. Atkinson fails to disclose **a first memory bus, a second memory bus, and a control system coupled to the first memory bus and the second memory bus, wherein**

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the control system is configured to operate the first memory bus and the second

memory bus at a first speed in the first power mode, and a second speed different than

the first speed in the second power mode. In Atkinson, the video graphics controller 2 is

coupled only to a first memory 5 and not a second memory. (See Figure 1). In view of the

above, Applicant respectfully submits that the above rejection of claim 1 under 35 U.S.C.

102(b) should be withdrawn.

Dependent claims 2-10 depend directly or indirectly upon independent claim 1.

Accordingly, dependent claims 2-10 are also allowable over the art of record.

Applicant submits that Atkinson fails to disclose the computer of amended

independent claim 11 and the device of amended independent claim 17. Atkinson fails to

disclose **a read-only memory, a second memory bus in communication with the read-

only memory,** and **a control system coupled to** the **second memory bus for reading the

read-only memory, the control system including a clock generator, wherein the control

system is configured to operate the first memory bus and the second memory bus at a

first clock speed in the first battery power mode, and a second clock speed greater than

the first clock speed in the second power mode.** Atkinson discloses a VRAM coupled to

the video graphics controller, but does not disclose a read-only memory coupled to the video

graphics controller configured to operate a second memory bus at a first and second clock

speed. In view of the above, Applicant respectfully submits that the above rejection of claims

11 and 17 under 35 U.S.C. 102(b) should be withdrawn.

Dependent claims 12-16 depend directly or indirectly upon independent claim 11.

Accordingly, dependent claims 12-16 are also allowable over the art of record. Dependent

claims 18-20 depend directly upon independent claim 17. Accordingly, dependent claims 18-

19 are also allowable over the art of record.

Applicant submits that the method of claim 21 is not disclosed by Atkinson. For the

same reasons as discussed above with reference to claim 1, Atkinson fails to disclose

**operating a first memory bus and a second memory bus at a first bus speed when the

mobile computing device is in the first power mode,** and **operating the first memory bus

and the second memory bus at a second bus speed different from the first bus speed

when the mobile computing device is in the second power mode.** In view of the above,

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Applicant respectfully submits that the above rejection of claim 21 under 35 U.S.C. 102(b) should be withdrawn.

Dependent claims 22-24 depend directly upon independent claim 21. Accordingly dependent claims 20-24 are also allowable over the art of record.

Claim Rejections under 35 U.S.C. § 103

Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Atkinson U.S. Patent No. 5,991,883 in view of Croughwell U.S. Patent No. 5,610,497.

Claim 20 depends directly upon independent claim 17, which Applicant submits is allowable over the art of record. Accordingly, dependent claim 20 is also allowable over the art of record.

Added Claims

Claim 27 has been added. Applicant submits that added claim 27 is not disclosed, taught, or suggested by Atkinson or Croughwell, either alone or in combination. Accordingly, applicant respectfully submits that added claim 27 is allowable over the art of record.

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CONCLUSION

In view of the above, Applicant respectfully submits that pending claims 1-24, and 27 are in form for allowance and are not taught or suggested by the cited references. Therefore, reconsideration and withdrawal of the rejections and allowance of claims 1-24, and 27 is respectfully requested.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed to either Gregg W. Wisdom at Telephone No. (360) 212-8052, Facsimile No. (360) 212-3069 or Steven E. Dicke at Telephone No. (612) 573-2002, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:
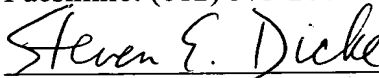
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Respectfully submitted,

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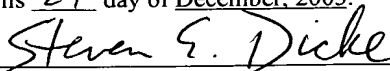Telephone: (612) 573-2002
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SED:jan

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CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service, as first class mail, in an envelope address to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 27 day of December, 2005.
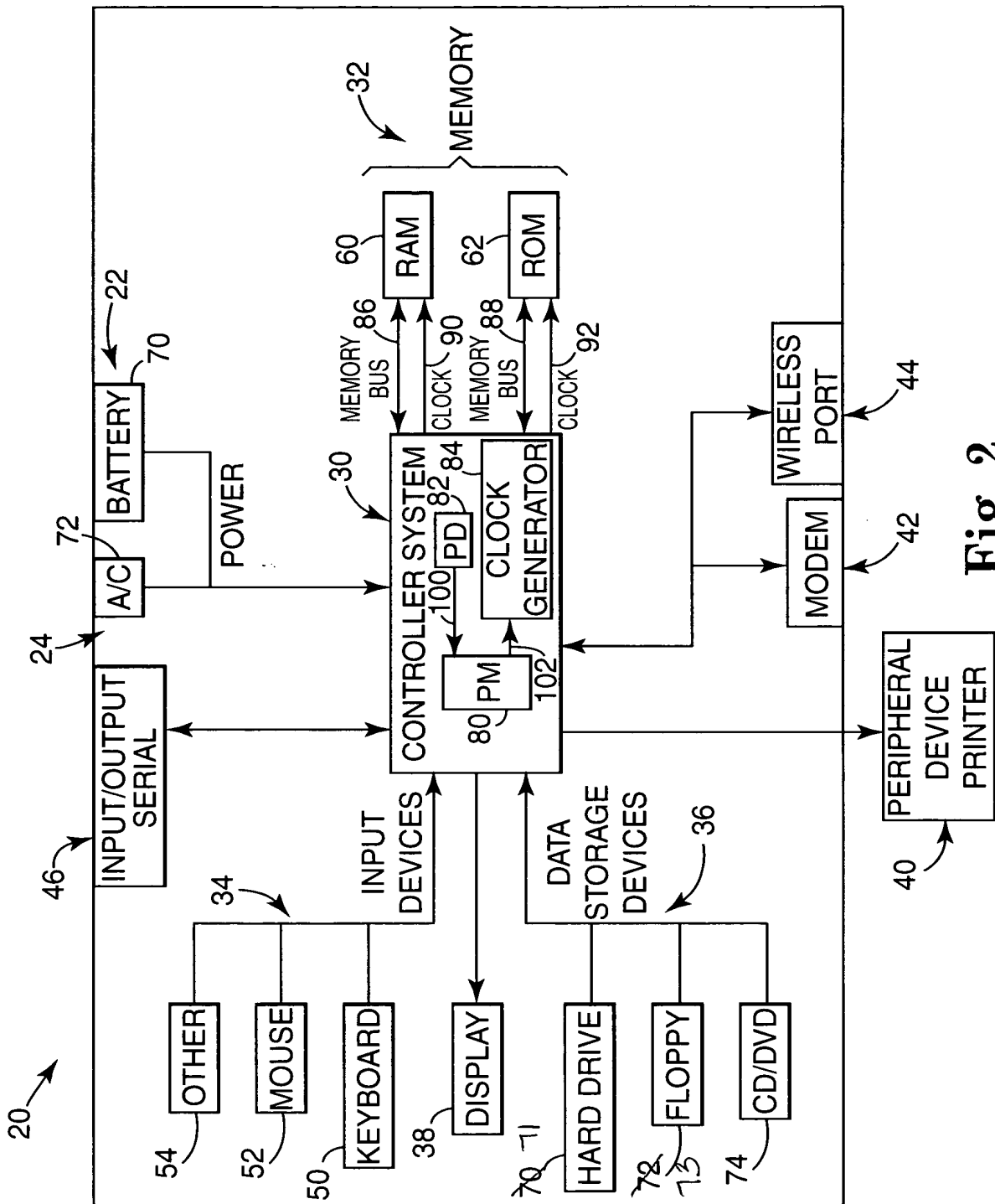
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Fig. 2

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IN THE DRAWINGS

Figure 2 has been amended to change the reference number of the hard disk drive from 70 to 71 and the reference number of the floppy disk drive from 72 to 73 to correct for the duplication of reference numbers.